

TFC Schedule

Initial population & testing (incl. PCI-A and memory access)	4/1/2001 to 5/15/2001
DSP testing* Booting, I/O & program testing	5/15/2001 to 6/15/2001
PCI-B and PCI-C no motherboard needed	6/15/2001 to 6/30/2001
Board-level I/O and control* using motherboard	6/30/2001 to 7/15/2001
Single events through TFC	7/15/2001 to 9/15/2001
System integration (where?)	9/15/2001 to 11/15/2001
Produce production boards	11/15/2001 to 12/15/2001
Stuffing and testing	12/15/2001 to 3/15/2002

*Biggest uncertainties

Comments:

Need DSP test code by 5/15 (Wendy has begun this)
Prefer a motherboard by 6/1 (system set up & prep)
Shop has hired another technician! (w/physics exp.)
New student beginning 5/15 (VxWorks set up?)

Hotlink Transmitter

Design & Schematics	4/1/2001 to 6/30/2001
Layout & prototype	7/1/2001 to 8/30/2001
Production*	9/1/2001 to 10/15/2001

Schedule driven by TFC constraints on shop
get LTB schematics & Altera from BU
get Cypress info from Nevis (SLIC)
same interface, different xmit format

Budget

Currently in budget (not FNAL)	\$222,000
Engineering remaining	\$40,000
TFC parts needed (18 boards)	\$80,000
TFC board manufacture	\$20,000
TFC stuffing	\$10,000
Transmitter? (18 boards?)	\$10,000
Remaining	\$62,000

Comments

Does not include motherboard or LRB

Go out for bids on parts next week (Univ. lead time)